

ABSTRACT

An integrated circuit for a computer peripheral device comprises an interface translator that receives subsystem control instructions from a computer, translates those instructions into an internal format understood by the subsystems of the peripheral device, and passes the instructions to the appropriate subsystem component. The peripheral device requires no processing capabilities. Instead, all of the data processing is performed by a host computer and the host computer's central processing unit (CPU). Appropriate computer peripheral devices for the present invention include printers, scanners, and devices having a combination of functions such as printing, faxing, and copying.